

**VIGNAN'S NIRULA INSTITUTE OF TECHNOLOGY & SCIENCE FOR WOMEN**  
**EXAMINATION BRANCH**

**III B.TECH - I SEMESTER – (R16) SUPPLEMENTARY EXAMINATIONS – MAY/JUNE, 2024**

**T I M E T A B L E**

**T I M E –A.N: 10.00 AM TO 1.00 PM**

<b>BRANCH</b>	<b>25-05-2024 (Saturday)</b>	<b>28-05-2024 (Tuesday)</b>	<b>06-06-2024 (Thursday)</b>	<b>10-06-2024 (Monday)</b>	<b>12-06-2024 (Wednesday)</b>
<b>EEE</b>	Pulse & Digital Circuits (RT161024)	Renewable Energy Sources (R1631022)	Power Systems-II (R1631021)	Signals & Systems (R1631023)	Power Electronics (R1631025)
<b>ECE</b>	Computer Architecture and Organization (R1631041)	Linear IC Applications (R1631042)	Digital Communications (R1631044)	Digital IC Applications (R1631043)	Antennas And Wave Propagation (R1631045)
<b>CSE</b>	Compiler Design (R1631051)	Unix Programming (R1631052)	Object Oriented Analysis and Design Using UML (R1631053)	Database Management Systems (R1631054)	Operating Systems (R1631055)
<b>IT</b>	Human Computer Interaction (R1631121)	Unix and Shell Programming (R1631052)	Advanced JAVA Programming (R1631122)	Database Management Systems (R1631054)	Operating Systems (R1631055)

**I/C EXAMCELL**  
 Date: 06-05-2024

**PRINCIPAL**

**VIGNAN'S NIRULA INSTITUTE OF TECHNOLOGY & SCIENCE FOR WOMEN**  
**EXAMINATION BRANCH**

**III B.TECH - I SEMESTER – (R19) SUPPLEMENTARY EXAMINATIONS – MAY/JUNE, 2024**

**T I M E T A B L E**

**T I M E –A.N: 10.00 AM TO 1.00 PM**

<b>BRANCH</b>	<b>25-05-2024</b> (Saturday)	<b>28-05-2024</b> (Tuesday)	<b>06-06-2024</b> (Thursday)	<b>10-06-2024</b> (Monday)	<b>12-06-2024</b> (Wednesday)	<b>14-06-2024</b> (Friday)
<b>EEE</b>	Linear IC Applications (R1931023)	Power Electronics (R1931022)	Power Systems-II (R1931021)	Digital Signal Processing (R1931024)	Microprocessors and Microcontrollers (R1931025)	
<b>ECE</b>	Linear Integrated Circuits and Applications (R1931041)	Microprocessor and Microcontrollers (R1931042)	Digital Communications (R1931043)	Electronic Measurements & Instrumentation (R1931044)		Digital System Design using HDL (R193104B)
<b>CSE</b>	Data Warehousing and Data Mining (R1931051)	Computer Networks (R1931052)	Compiler Design (R1931053)	Artificial Intelligence (R1931054)		Advanced Data Structures (R193105C)
<b>IT</b>	Advanced Data Structures (R1931121)	Computer Networks (R1931122)	Compiler Design (R1931123)	Artificial Intelligence (R1931054)	Design and Analysis of Algorithms (R1931124)	Software Testing Methodologies (R193105D)

**I/C EXAMCELL**

Date: 06-05-2024

**PRINCIPAL**

**VIGNAN'S NIRULA INSTITUTE OF TECHNOLOGY & SCIENCE FOR WOMEN**  
**EXAMINATION BRANCH**

**III B.TECH - I SEMESTER – (R20) SUPPLEMENTARY EXAMINATIONS – MAY/JUNE, 2024**  
**T I M E T A B L E**

**T I M E –A.N: 10.00 AM TO 1.00 PM**

<b>BRANCH</b>	<b>25-05-2024</b> (Saturday)	<b>28-05-2024</b> (Tuesday)	<b>06-06-2024</b> (Thursday)	<b>10-06-2024</b> (Monday)	<b>12-06-2024</b> (Wednesday)
<b>EEE</b>	Power Systems-II(R2031021)	Power Electronics (R2031022)	Control Systems (R2031023)	Utilization Of Electrical Energy (R203102B)	Object Oriented Programming through JAVA(R203105G)
<b>ECE</b>	Analog ICs and Applications (R2031041)	Electromagnetic Waves and Transmission Lines (R2031042)	Digital Communications (R2031043)	Computer Architecture & Organization (R203104C)	Operating Systems (R203105L)
<b>CSE</b>	Computer Networks (R2031051)	Design and Analysis of Algorithms (R2031052)	Data Warehousing and Data Mining (R2031053)	Artificial Intelligence (R203105A)	Digital Logic design (R203104Q)
<b>IT</b>	Computer Networks Lab(R2031055)	Design and Analysis of Algorithms (R2031052)	Data Mining Techniques (R2031121)	Artificial Intelligence (R203105A)	Digital Logic design (R203104Q)